



01P-2815
PATENT
Case Docket No. FILLF1.001CF1
Date: September 19, 2001
Page 1
SEP 27 2001
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C 2800 MAIL ROOM

In re application of : Bart Dierickx
App. No. : 09/460,630
Filed : December 14, 1999
For : BURIED, FULLY
DEPLETABLE, HIGH FILL
FACTOR PHOTODIODES
Examiner : Eugene Lee
Art Unit : 2815

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

Sept 19, 2001
(Date)

Stephen C. Jensen
Stephen C. Jensen, Reg. No. 35,556

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

Sir:

Transmitted herewith is an amendment in the above-identified application.

(X) An extension of time to respond for 1 month is hereby requested.

Time Extension Fee:

(X) one month (\$110 large entity)
() two months (\$390 large entity)
() three months (\$890 large entity)

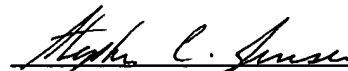
The fee has been calculated as shown below:

CLAIMS AS FILED						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	11	—	20	= 0 ×	\$18	= \$0
Independent Claims	1	—	3	= 0 ×	\$80	= \$0
If application has been amended to contain multiple dependent claim(s), then add					\$270	= \$0
Time Extension Fee						\$110
09/26/2001 MWOLDER1 00000153 09460630				TOTAL ADDITIONAL FEE		
01 FC:115 110.00 0P				FOR THIS AMENDMENT		
						\$110

(X) Return prepaid postcard.

(X) A check in the amount of \$110 is enclosed.

-
- (X) Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.



Stephen C. Jensen

Registration No. 35,556

Attorney of Record

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Group Art Unit 2815

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5,556

Dear Sir:

In response to the Office Action mailed May 23, 2001, please amend the above-captioned patent application as follows:

Please amend the paragraph starting at page 5, line 8 as follows:

In another embodiment, the surface regions of the semiconductor substrate beyond the collection region are barrier regions which have dopants of the first conductivity type at a concentration density larger than the concentration density of the semiconductor substrate, and the read-out electronics are formed within shielding regions. Preferably, at least part of the charge carriers that are generated in the semiconductor substrate underneath the shielding regions are collected by the collection region.

Please amend the paragraph starting at page 5, line 15 as follows: